

**REMARKS/ARGUMENTS**

Reconsideration and withdrawal of the rejections of the application are respectfully requested in view of the amendments and remarks herewith, which place the application in compliance for allowance. The present amendment is made to facilitate prosecution of the application.

**I. STATUS OF THE CLAIMS AND FORMAL MATTERS**

Claims 1-21 are pending in this application. Claims 1, 8, and 15 are independent and hereby amended. Support for this amendment is provided throughout the Specification as originally filed and specifically at pages 17-20 (paragraphs [0086]-[0106]). No new matter has been introduced by this amendment. Changes to the claims are not made for the purpose of patentability within the meaning of 35 U.S.C. §101, §102, §103, or §112. Rather, these changes are made simply for clarification and to round out the scope of protection to which Applicants are entitled.

**II. REJECTIONS UNDER 35 U.S.C. 103(a)**

Claims 1-5, 7-12, 14-19 and 21 were rejected under 35 U.S.C. §103(a) as allegedly anticipated by U.S. Patent No. 6,226,038 to Frink et al. (hereinafter, merely "Frink").

Claims 6, 13 and 20 were rejected under 35 U.S.C. §103(a) as allegedly unpatentable over Frink in view of U.S. Patent Application Publication 2002/0168036 A1 to Kim (hereinafter, merely "Kim").

### III. RESPONSE TO REJECTIONS

Claim 1 recites, *inter alia*:

**“...a first PCI card, which includes a first decoder and a second decoder** which decompress respective compressed high-definition television video data transferred from the computer; and

**a second PCI card, which includes edit processing means** for performing edit processing on the high-definition television video data decompressed by the first decoder and the high-definition television video data decompressed by the second decoder...

**...wherein the first PCI card and the second PCI card have PCI connectors for connecting with a motherboard of the computer by the PCI connectors, and are installed in PCI slots of the computer.**” (Emphasis added)

Applicants submit that neither Frink nor Kim, taken alone or in combination, that would teach or suggest the above identified features of claim 1. Specifically, neither of the references used as a basis for rejection describes that a first PCI card includes a first decoder and a second decoder, a second PCI card includes edit processing means, and the first PCI card and the second PCI card have PCI connectors for connecting with a motherboard of the computer by the PCI connectors, and are installed in PCI slots of the computer, as recited in claim 1.

Specifically, the Office Action (see page 3) asserts that Frink discloses that the HD video system is attached or connected to a computer mother board through a PCI, and refers to Fig. 5, which is reproduced as follow:

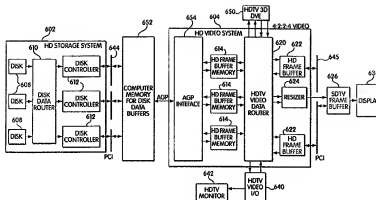


Fig. 6

Thus, Fig. 5 in Frink mentions PCI sort for PCI local bus, which specifies a computer bus for attaching peripheral devices to a computer mother board, *i.e.*, the HD video system 504 is attached or connected to the computer mother board through PCI bus (see Office Action, page 3, paragraph "Frink discloses wherein..."), however, **Frink teaches nothing about the HD video system is contained on a PCI card which is installed in a PCI slot of the computer mother board.**

However, in the present invention, pages 17-20, paragraphs [0086]-[0106] of Applicants' corresponding published application describe three PCI cards installed in the PCI slots of the workstation as shown in Figures 2-3, which are reproduced as follow:

[0086] Three PCI cards are installed in the PCI slots of the workstation 1. FIG. 2 is a schematic view illustrating the three PCI cards 8, 14, and 16.

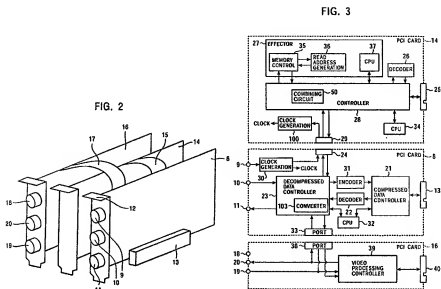
[0087] The PCI card 8 is used to input/output HDTV data and to decode HDCAM-format HDTV video data (hereinafter referred to as "HD compressed data"). The PCI card 8 has a PCI connector 13 for connection with a board (hereinafter referred to as a "motherboard"), which has PCI slots, in the computer, and has a securing-mechanism component 12 for securing the computer with a screw.

[0089] The PCI card 14 is used mainly to input/output HDTV data, to decode HD compressed data, and to perform edit-processing on HDTV data. The PCI card 14 also has a PCI connector 25 (shown in FIG. 3, but not shown in FIG. 2) and a securing mechanism component, which are the same as those of the PCI card 8.

[0094] FIG. 3 is a block diagram illustrating the circuit configuration of the PCI cards 8, 14, and 16. The PCI card 8 includes a compressed-data controller 21, a decoder 22, a decompressed-data controller 23, a clock generation circuit 30, an encoder 31, and a CPU 32 for controlling the individual blocks of the PCI card 8.

[0100] The PCI card 14 includes a decoder 26, an effector 27, and a controller 28, a CPU 34 for controlling the individual blocks of the PCI card 14, and a clock generation circuit 100.

[0104] The PCI card 16 includes a video processing controller 39. The video processing controller 39 is a circuit for controlling the input/output of uncompressed SDTV data via the PCI connector 40, the input connector 19, the output connector 20, and the port 38 (for sending/receiving uncompressed SDTV data to/from the PCI card 8).



Thus, in the present invention, the PCI cards 8 and 16, which include two decoders and edit processing means, have PCI connectors and are installed in the slots of the mother board of the computer, so that the HDTV signal can be edited in real time without the use of dedicated large hardware other than a computer (See, Sepecification, page 3, paragraph [0015]).

Thus, nothing has been found in Frink that would teach a first PCI card includes a first decoder and a second decoder, a second PCI card includes edit processing means, and the first PCI card and the second PCI card have PCI connectors for connecting with a motherboard of the computer by the PCI connectors, and are installed in PCI slots of the computer, as recited in claim 1.

Therefore, Applicants respectfully submit that claim 1 is patentable.

For reasons similar to those described above with regard to independent claim 1, independent claims 8 and 15 are also patentable.

#### **IV. DEPENDENT CLAIMS**

The other claims in this application are each dependent on an independent claim discussed above, and are therefore believed patentable for at least the same reasons. Since each dependent claim is also deemed to define an additional aspect of the invention, however, the individual reconsideration of the patentability of each on its own merits is respectfully requested.

Similarly, because Applicants maintain that all claims are allowable for at least the reasons presented hereinabove, in the interests of brevity, this response does not comment on each and every comment made by the Examiner in the Office Action. This should not be taken as acquiescence of the substance of those comments, and Applicants reserve the right to address such comments.

#### **CONCLUSION**

In the event the Examiner disagrees with any of statements appearing above with respect to the disclosures in the cited reference or references, it is respectfully requested that the

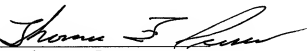
Examiner specifically indicate those portions of the reference, or references, providing the basis for a contrary view.

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In view of the foregoing amendments and remarks, it is believed that all of the claims in this application are patentable and Applicants respectfully request early passage to issue of the present application.

Respectfully submitted,

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